

Single N-Channel MOSFET

■ DESCRIPTION

SMC4812 is the N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior, fast switching performance. These devices are well suited for high efficiency fast switching applications.

■ PART NUMBER INFORMATION

SMC 4812 M - TR G

a b c d e

a : Company name.

b : Product Serial number.

c : Package code M:SOP-8

d : Handling code TR:Tape&Reel

e : Green produce code G:*RoHS Compliant*

■ FEATURES

$V_{DS} = 30V, \quad I_D = 10.7A$

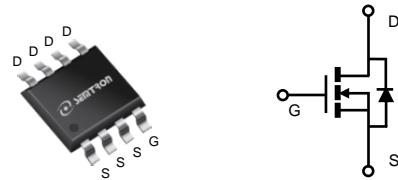
$R_{DS(ON)}=14m\Omega(Typ.)@V_{GS}=10V$

$R_{DS(ON)}=21m\Omega(Typ.)@V_{GS}=4.5V$

- ◆Fast switch
- ◆Improved dv/dt capability
- ◆High power and current handling capability

■ APPLICATIONS

- ◆LED Lighting
- ◆Power Management
- ◆DC-DC Power System
- ◆Load Switch



SOP-8

■ ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-Source Voltage	30	V
V_{GSS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	$T_A=25^\circ C$ $T_A=70^\circ C$	10.7 8.6
I_{DM}	Pulsed Drain Current ^A	42.8	A
I_{AS}	Avalanche Current ^A	20	A
E_{AS}	Single Pulse Avalanche energy L=0.1mH ^{AD}	20	mJ
P_D	Power Dissipation ^B	$T_A=25^\circ C$ $T_A=70^\circ C$	3.1 2
T_J	Operation Junction Temperature	-55/150	°C
T_{STG}	Storage Temperature Range	-55/150	°C

■ THERMAL RESISTANCE

Symbol	Parameter	Typ	Max	Units
$R_{\theta JA}$	Thermal Resistance Junction to Ambient ^B	$t \leq 10s$	40	°C/W
	Thermal Resistance Junction to Ambient ^{BC}	Steady-State	65	

ELECTRICAL CHARACTERISTICS($T_A = 25^\circ\text{C}$ Unless otherwise noted)

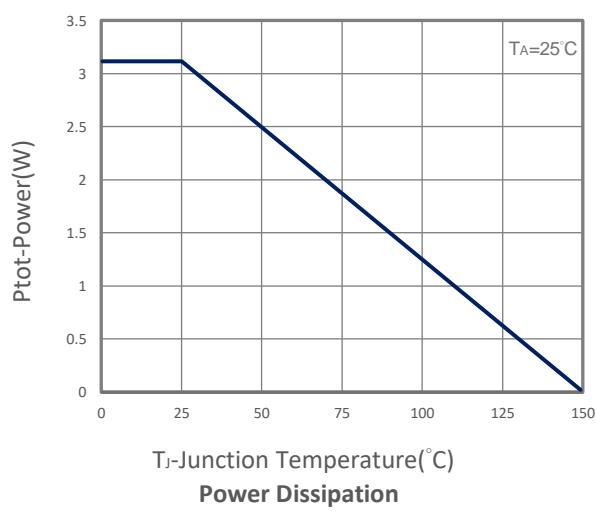
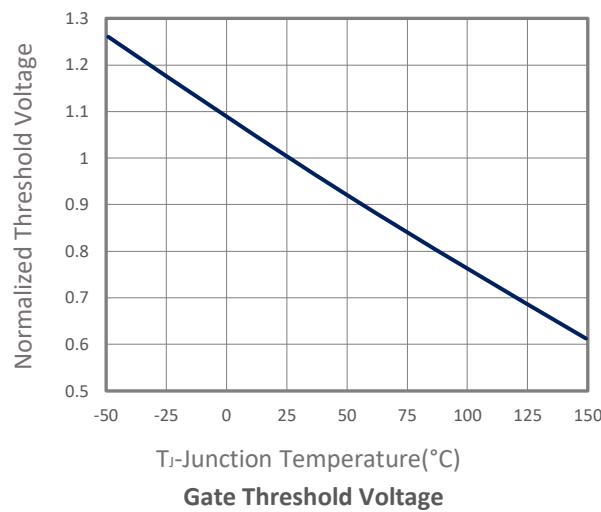
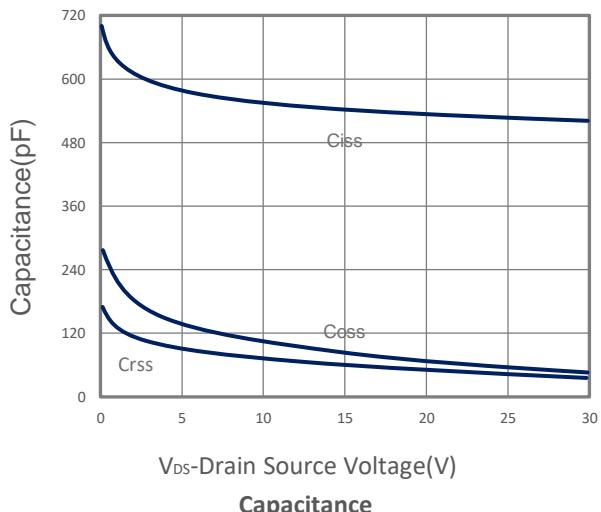
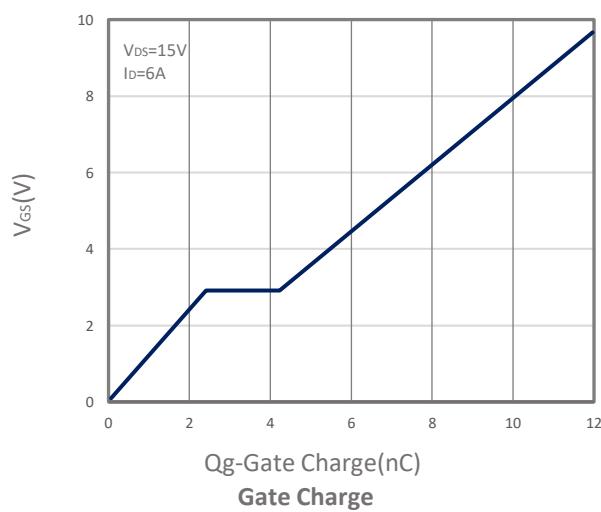
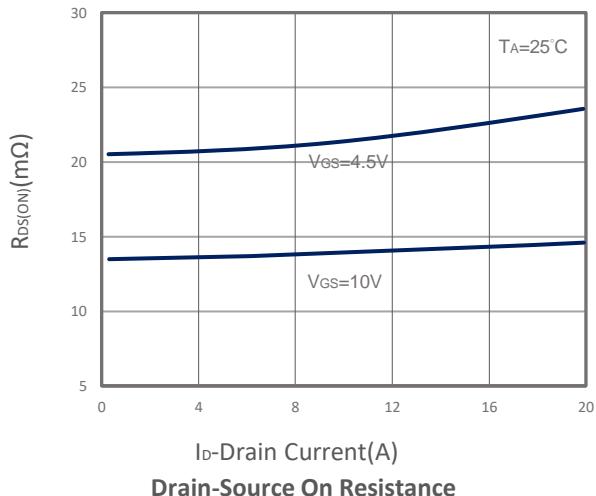
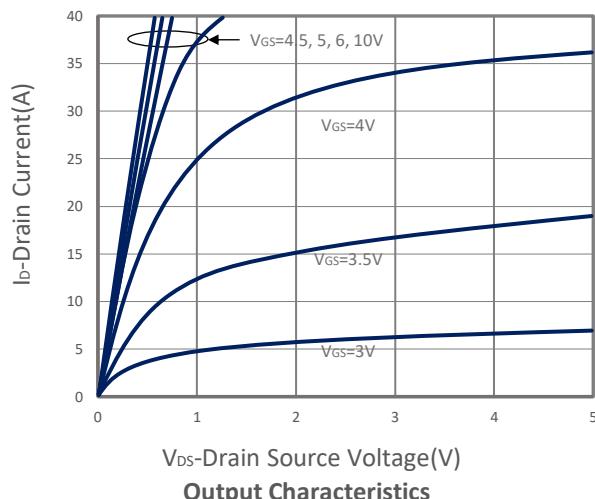
Symbol	Parameter	Condition	Min	Typ	Max	Unit	
Static Parameters							
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	30			V	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1	1.6	2.5	V	
I_{GSS}	Gate Leakage Current	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			± 100	nA	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=30\text{V}, V_{GS}=0\text{V}, T_J=25^\circ\text{C}$		1		μA	
		$V_{DS}=24\text{V}, V_{GS}=0\text{V}, T_J=75^\circ\text{C}$		10			
$R_{DS(\text{ON})}$	Drain-source On-Resistance	$V_{GS}=10\text{V}, I_D=10.7\text{A}$		14	17	$\text{m}\Omega$	
		$V_{GS}=4.5\text{V}, I_D=8\text{A}$		21	27		
G_f	Forward Transconductance	$V_{DS}=15\text{V}, I_D=6\text{A}$		6		S	
Diode Characteristics							
V_{SD}	Diode Forward Voltage	$I_S=1\text{A}, V_{GS}=0\text{V}$			1	V	
I_S	Diode Continuous Current				10.7	A	
t_{rr}	Reverse Recovery Time	$I_S=6\text{A}, dI/dt=100\text{A}/\mu\text{s}$		12.5		ns	
Q_{rr}	Reverse Recovery Charge		$T_J=25^\circ\text{C}$	3.2		nC	
Dynamic and Switching Parameters							
Q_g	Total Gate Charge	$V_{DS}=15\text{V}, V_{GS}=10\text{V}, I_D=6\text{A}$		12.7	17.8	nC	
Q_g	Total Gate Charge(4.5V)			6.2	8.7		
Q_{gs}	Gate-Source Charge			2.4	3.4		
Q_{gd}	Gate-Drain Charge			2	2.8		
C_{iss}	Input Capacitance	$V_{DS}=15\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$		550		pF	
C_{oss}	Output Capacitance			78			
C_{rss}	Reverse Transfer Capacitance			62			
R_g	Gate Resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$		2.4		Ω	
$t_{d(on)}$	Turn-On Time	$V_{DD}=15\text{V}, V_{GEN}=10\text{V}, R_G=3.3\Omega, I_D=1\text{A}$		2.5	5	nS	
				7.6	14		
$t_{d(off)}$	Turn-Off Time			19.8	38		
				4.2	8		

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

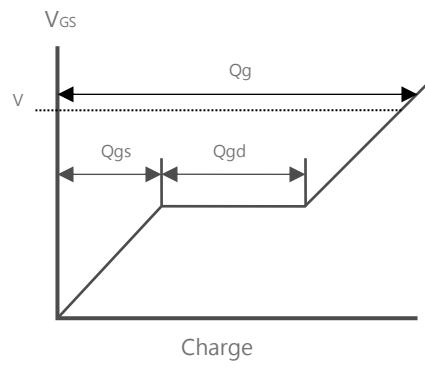
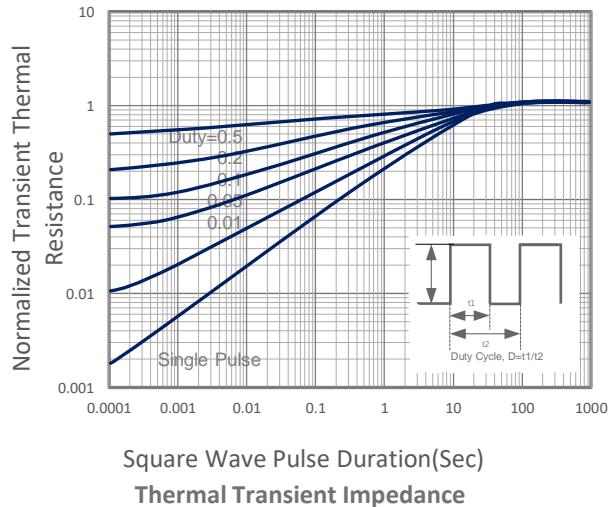
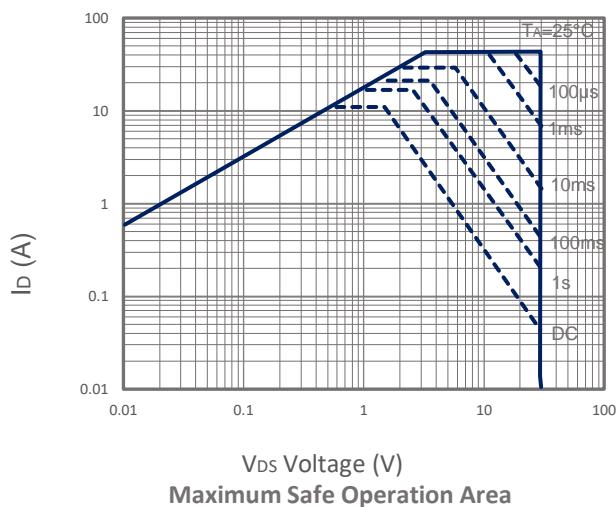
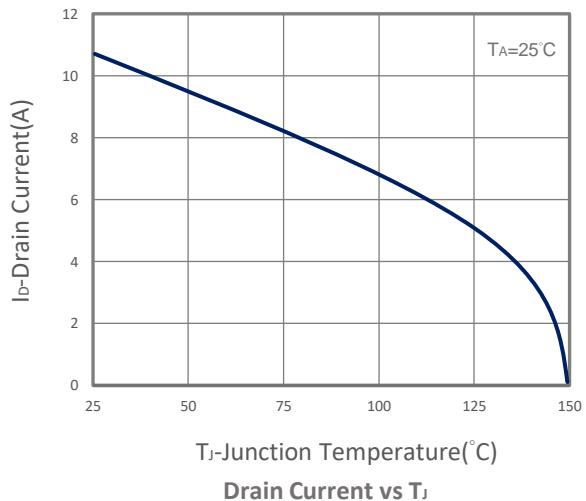
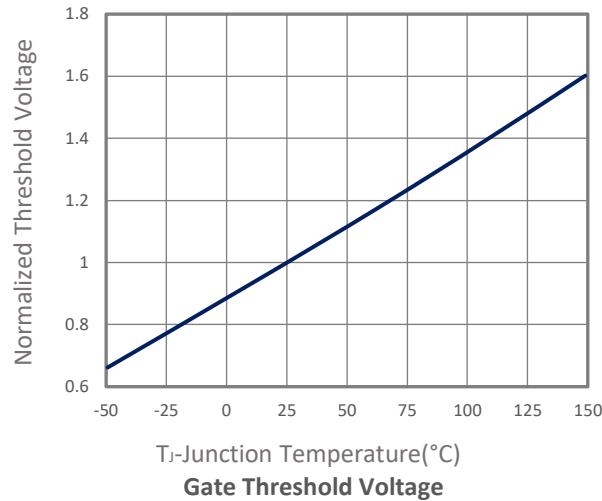
- A. Pulsed width limited by maximum junction temperature, $T_J(\text{MAX})=150^\circ\text{C}$.
- B. The value of R_{eJA} is measured with the device mounted on 1in2 FR-4 board in a still air environment with maximum junction temperature $T_J(\text{MAX})=150^\circ\text{C}$ (initial temperature $T_A=25^\circ\text{C}$).
- C. $T_J(\text{MAX})=150^\circ\text{C}$, using junction-to-case thermal resistance (R_{eJC}) is more useful in additional heat sinking is used.
- D. The EAs data shows Max, tested and pulse width limited by $T_J(\text{MAX})=150^\circ\text{C}$ (initial temperature $T_J=25^\circ\text{C}$).

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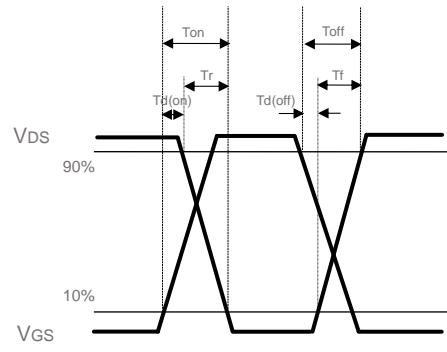
TYPICAL CHARACTERISTICS



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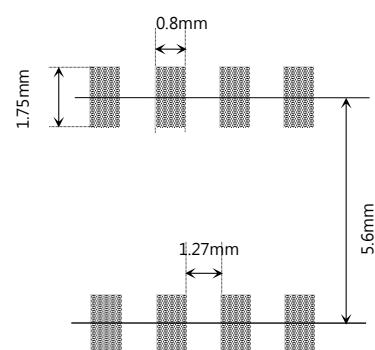
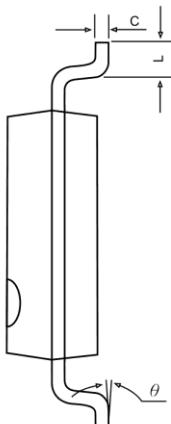
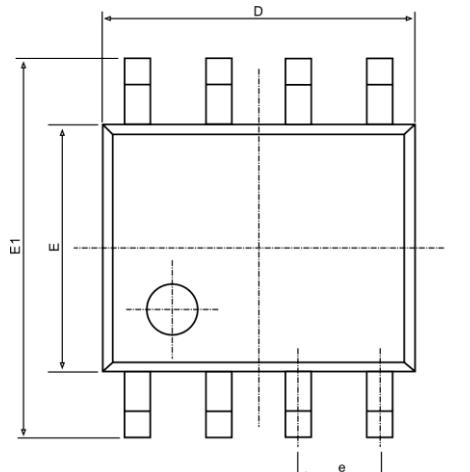


Gate Charge Waveform

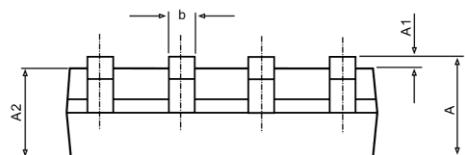


Switching Time Waveform

SOP-8 PACKAGE DIMENSIONS



Recommended Land Pattern



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.040	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.130	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270BSC.		0.050BSC.	
L	0.400	1.270	0.016	0.005
Θ	0°	8°	0°	8°